

TRADEMAKERS
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EX-1
EAY 2183 AW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

David K. Vavro et al.

RECEIVED

AUG 19 2002

Serial No.: 09/465,634

Technology Center 2100

Filed: December 17, 1999

Examiner: Stephanie Deckter

For: Digital Signal Processor Having a
Plurality of Independent Dedicated
Processors

Atty Docket: ITL.0286US
P7814

Commissioner for Patents
Washington, DC 20231

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REPLY TO PAPER NO. 2

Sir:

In response to the office action mailed July 8, 2002, please amend the above-referenced patent application as follows:

IN THE SPECIFICATION

Please amend the paragraph beginning on page 5, line 1, as follows:

A1 The programmable random access memory (RAM) processor (PRP) 24 basically stores and retrieves data. It may be particularly advantageous in storing intermediate results, in a cache-like fashion. This may be particularly applicable in two-dimensional image processing.

IN THE DRAWINGS

The proposed drawing corrections are indicated in red on the attached sheets.

IN THE CLAIMS

Please cancel claims 25-30.

Date of Deposit: August 9, 2002

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents Washington DC 20231.

Cynthia L. Hayden
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